RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/690235 Filing Date: October 21, 2003

Title: BIDIRECTIONAL PORT WITH CLOCK CHANNEL USED FOR SYNCHRONIZATION

Assignee: Intel Corporation

REMARKS

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This responds to the Office Action mailed on February 24, 2004.

No claims are amended. Claims 1-9 are currently pending in this application.

Double Patenting Rejection

Claims 1-9 were provisionally rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1, 3, 4 and 6-11 of copending U.S. Patent Application Serial No. 09/894,865.

A Terminal Disclaimer in compliance with 37 CFR 1.321(b)(iv) is enclosed herewith to obviate this rejection.

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Assignee: Intel Corporation

Conclusion

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney ((612) 349-9581) to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

MATTHEW B. HAYCOCK ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Attorneys for Intel Corporation

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P.O. Box 2938

Minneapolis, Minnesota 55402

(612) 349-958]

Date *Apr.* 22 04

John M. Dahl

Reg. No. 44,639

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 23 day of April 2004.

Anne M. Richards	

Name

Signature